YEAR/SEM : II/II B.TECH (R-16)

LAB CODE/LAB NAME : EE406ES CONTROL SYSTEMS & SIMULATION LAB

INCHARGE (TEACHER/LAB STAFF) : A.SREELATHA REDDY / K NARESH

LIST OF EXPERIMENTS

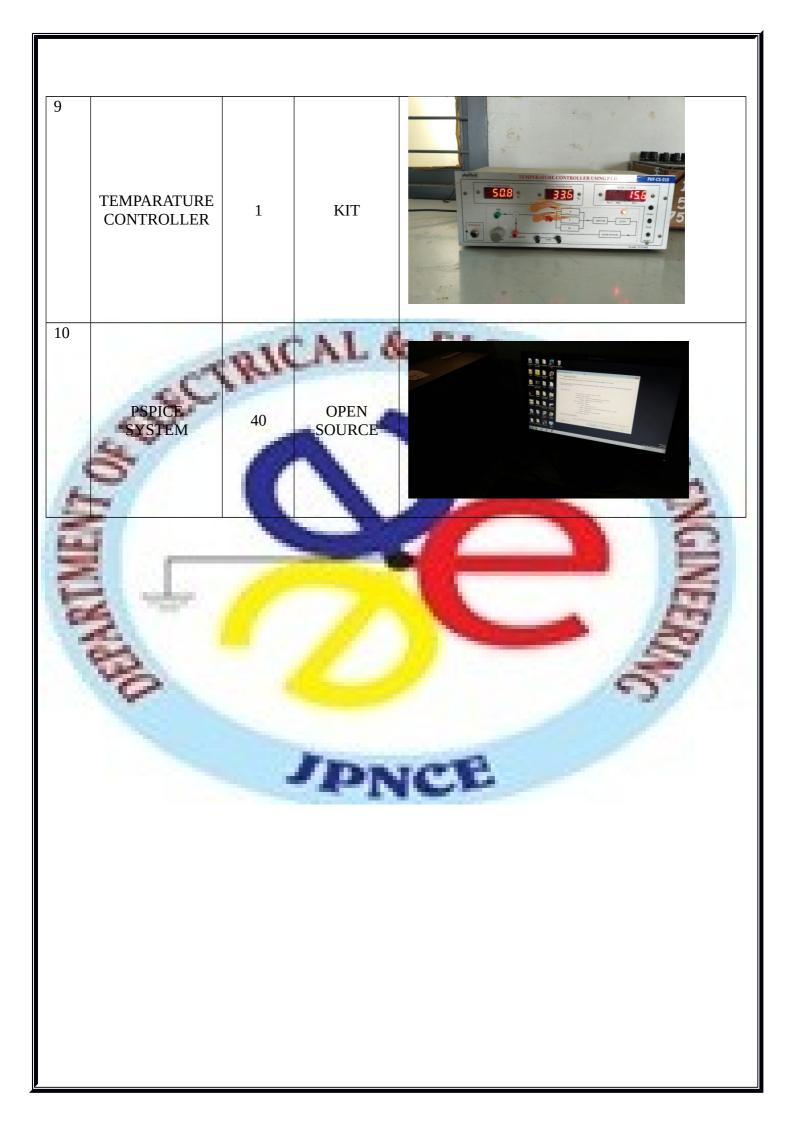
CYCLE – I

- 1. TIME RESPONSE OF SECOND ORDER SYSTEM
- CHARACTERIST.
 PROGRAMMABLE LOGIC CO...
 TABLES OF LOGIC GATES, SIMPLE BOOLL...
 CONTROL OF MOTOR
 EFFECT OF FEEDBACK ON DC SERVO MOTOR
 TRANSFER FUNCTION OF A DC MOTOR
 TRANSFER FUNCTION OF A DC GENERATOR 3. PROGRAMMABLE LOGIC CONTROLLER – STUDY AND VERIFICATION OF TRUTH TABLES OF LOGIC GATES, SIMPLE BOOLEAN EXRESSION AND APPLICATION OF SPEED

- EFFECT OF P,PD,PI,PID CONTROLLER ON A SECOND ORDER SYSTEMS
- 10. LAG AND LEAD COMPENSATION MAGNITUDE AND PHASE PLOT
- 11. a) SIMULATION OF P,PI,PID CONTROLLER,
- b) LINEAR SYSTEM ANALYSIS (TIME DOMAIN ANALYISIS) USING SUITABLE SOFTWARE
- 12. STABILITY ANALYS<mark>IS (BODE ROOT LOCUS, NYO</mark>IST) OF LINEAR TIME INVARIANT SYSTEM USING SUITABLE SOFTWARE
- 13. STATE SPACE MODEL FOR CLASSICAL TRANSFER FUNCTION USING SCILAB **VERIFICATION**
- 14. DESING OF LEAD-LAG COMPEN SATOR FOR THE GIVEN SYSTEM AND WITH SPECIFICATION USING SUITABLE SOFTWARE
- 15. LTSPICE OF SIMULATION OF OP AMP BASED INTEGRATOR AND DIFFERENTIATOR CIRCUITS
- 16. LINEAR SYSTEM ANALYSIS (TIME DOMAIN ANALYSIS, ERROR ANALYSIS) USING SCILAB
- 17. STABILITY ANALYSIS (BODE PLOT, ROOT LOCUS, NYQUIST) OF LINEAR TIME INVARIANT SYSTEM USING SO

S.N o	Equipment	Quanti ty	Specificati ons	Image
1	Time response system a)DRB b)FUNCTION GENERATOR c)CRO(0-20MHZ)	06 02 02	0-100MΩ 0-1MHZ 0-20MHZ	
2	SYNCROUS		KIT	
3	PLC	1	KIT	
4	DC SERVO MOTOR	1	DC SERVO MOTOR	D. C. POSITION SONO TRAINER UNIT

5	TRASFOR FUNCTION	1	DC MOTOR EEE LADY TIVES	
6	TRANSFER FUNCTION SERVO MOTOR	PAN	DC GENERATO R AC MOTOR AC MOTOR	Generator A * * * * * * * * * * * * * * * * * *
8	LAG AND LEAD COMPENSATIO N	1	MAGNITUD E PHASE PLOT	



REF: JPNCE/EEE/02

JAYA PRAKASH NARAYAN COLLEGE OF ENGINEERING DHARMAPUR, MAHBUBNAGAR DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING CONTROL SYSTEMS & SIMULATION LAB B.TECH HI- H SEM (R-15) - EEE LIST OF EXPERIMENTS CYCLE - I

- 1. TIME RESPONSE OF SECOND ORDER SYSTEM
- 2. CHARACTERISTICS OF SYNCHROS
- 3. PROGRAMMABLE LOGIC CONTROLLER STUDY AND VERIFICATION OF TRUTH TABLES OF LOGIC GATES, SIMPLE BOOLEAN EXRESSION AND APPLICATION OF
 - SPEED CONTROL OF MOTOR
- 4. EFFECT OF FEEDBACK ON DC SERVO MOTOR
- 5. TRANSFER FUNCTION OF A DC MOTOR
- 6. TEMPERATURE CONTROLLER USING PID

CYCLE - II

- 7. CHARACTERISTIC OF MAGNETIC AMPLIFIER
- 8. TRANFER FUNCTION OF A DC GENERATOR
- 9. CHARACTERISTICS OF AC SERVO MOTOR
- 10. EFFECT OF P,PD,PI,PID CONTROLLER ON A SECOND ORDER SYSTEMS
- 11. LAG AND LEAD COMPENSATION MAGNITUDE AND PHASE PLOT
- 12. A) SIMULATION OF P,PI,PID CONTROLLER,
 - B) LINEAR SYSTEM ANALYSIS (TIME DOMAIN ANALYISIS) USING SUITABLE SOFTWARE
- 13. STABILITY ANALYSIS (BODE PLOT, ROOT LOCUS, NYQUIST) OF LINEAR TIME INVARIANT SYSTEM USING SUITABLE SOFTWARE
- 14. STATE SPACE MODEL FOR CLASSICAL TRANSFER FUNCTION USING SCILAB VERIFICATION
- 15. DESING OF LEAD-LAG COMPENSATOR FOR THE GIVEN SYSTEM AND WITH SPECIFICATION USING SUITABLE SOFTWARE
- 16. LTSPICE OF SIMULATION OF OP AMP BASED INTEGRATOR AND DIFFERENTIATOR CIRCUITS
- 17. LINEAR SYSTEM ANALYSIS (TIME DOMAIN ANALYSIS, ERROR ANALYSIS) USING SCILAB

18. STABILITY ANALYSIS (BODE PLOT, ROOT LOCUS, NYQUIST) OF LINEAR TIME INVARIANT SYSTEM USING SCILAB

